

XRM-IO146 User Manual

Revision: V2.0



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Head Office

US Office

Address: 4 West Silvermills Lane, Edinburgh, EH3 SBD, UK
Telephone: +44 131 558 2600
Fax: +44 131 558 2700
email: sales@alpha-data.com
http://www.alpha-data.com

3507 Ringsby Court Suite 105, Denver, CO 80216 (303) 954 8768 (866) 820 9956 toll free sales@alpha-data.com http://www.alpha-data.com

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1 Introduction



Figure 1: XRM-IO146



Figure 2: XRM-IO146-ROCKET

The XMh.U148 provides 148 (IV signals on a front panel MiCTOR connector with a wide range of options to support GPIO requirements. Signals are drivent received by the FPGA for total user programmability and can be used single ended (i.e. LVTIL) or in pairs (i.e. LVDS). Micro-coax cable assemblies for this adapter can support signals up to 2GHz (also available from Alpha Data). For positive locking cable retention, an optional cable clamp assembly is available.

Each pair of I/O signals is routed as shown below using optional fit resistors. The default manufacturing option is Bes-Off and Rt not fitted, so that the signals pass straight intrough to an FPGA of their options are available by contacting Alpha-Data and requesting a custom part ordering code. Alpha Data can also provide further information to rework the termination resistors at oustomer sites if IPC-A-610 specialists are available. Rs can be used to provide series dampening for port to point applications, or current limiting, but for LVDS is hybically 6R0. Rt can be used for LVDS inputs to provide the termination voltage from the line current, or parallel termination; although Alium FPGAs from Virtex-4 convent flowers the very series of the remainstance of the size of the convention of the provided of the convention of the size of the convention of the co

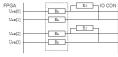


Figure 3: XRM-IO146 Terminaion Options

Each single ended signal is routed via a series resistor. These are 33R0 by default, but can be customized by Alpha Data or at customer sites.



1.1 Specifications

The XRM-IO146 provides multiple channels of bidirectional I/O capability with complete programmability from the FPGΔ

- Standard XRM format module (Alpha Data standard)
- Compatible with all Alpha Data boards featuring an XRM site
- 64 differential (LVDS, LVPECL, etc.) pairs + 4 clock capable pairs (LVDS, LVPECL, etc.) + 10 single ended (LVTTL, LVCMOS, etc.)
- · All signals can be used single ended (up to 146 LVTTL, LVCMOS, etc.)
- · XRM VCCIO selectable from the FPGA board at 1.2V to 3.3V (depending on limits of FPGA family)
- · Battery holders to provide power for FPGA encryption key memory
- . Option for 5V power output fused at 1.0 amp
- Option for 7x Rocket(O lanes (7x Tx/Rx pairs) in lieu of 13 LVDS pairs and 2 5V output pins
- Option for positive locking cable clamp assembly



Figure 4: XRM-IO146 Pin Diagram

2 Installation

2.1 Fitting

The XRM-IO146 is designed to plug in to the front panel connector (SAMTEC QSH series) on Alpha Data cards with an XRM site. Note that Virtex-6 and newer boards key this connector 180deg from Virtex-5 and earlier boards. For Virtex-6 and up, the XRM2 version is required and can't be interchanged with the XRM version for Virtex-5 and earlier boards. The retaining screws should be tightened to secure the XRM-IO146.

This operation should not be performed while the card is powered up.

2.2 Handling instructions

Observe precautions for preventing damage to components by electrostatic discharge. Personnel handling the board should take ESD precautions.

Page 2 Installation



Note:

Avoid flexing the board.

2.3 Voltage Settings

Note:

This board will pass signals directly to the FPGA unless termination options are specified; users should ensure that the VIO setting on the FPGA card is set to suit the appropriate signaling level to ensure no damage can occur. Also ensure that the signals applied to this module are within the limits of the FPGA IOBs.

2.4 Battery Installation

The XRM-IO146 has 2 battery holders for supplying the VBATT power supply to maintain the encryption key memory within the XIM: PFDA. If the encryption feature of the FPCA into Iuce, then no batteries need to be installed. Use 15V SR60 coin cell batteries, 6.8mm, silver oxide such as Duracell D344 (not included by Alpha Data). When replacing the batteries, essure one is always installed with lere legicing the other and her VBATT supply will continuously keep the encryption key backed up. Refer to the XIIInx configuration user guide for further information.

3 XRM-IO146 Interface Connector

The XRM-IO146 signal interface is a MICTOR 152-way receptacle connector, AMP/TYCO part number 5767044-4 (or equivalent).

Suggested mating part is AMP/TYCO 1-5767007-1 152 pin Mictor Plug (or similar).

Cable assemblies are available from Alpha Data or directly from Micro Interconnects (www.microinterconnects.com). Alpha Data can provide standard cable assemblies for 152M to 152M (50cm), 152M to 4 x 38M (50cm), or custom cabling solutions as requested.

4 XRM-IO146 Interface - Pinout Tables

The front pamel MICTOR connector is broken up into 4 banks, which can be used for a single function or split up as required for an application (see coloning of banks in diagrams blevely. Calling can be used to break off each bank to its own connector; connected to another 152-pin MICTOR; or customized for a specific application. The following tables shows the MICTOR connector prins, connection to the Samters CAMR connector and signal description. See the Alpha Data SDK for the UCF files to map each pin to the FPGA pad, or contact Alpha Data for further support.

- The first letter in the signal name (d or s) represents differential or single ended routing. RocketlO signals begin with (gtp).
- The second letter (a,b,c,d) represent the bank of the MICTOR connector. RocketIO signals specify the direction relative to the FPGA (tx or rx).
- Each pair is represented by _p / _n indicating polarity and a pair number.
- Clock capable signals (cc in name) can be used for differential clocks, single ended clocks, or data.
 Additional clock capable signals may be available depending on the main FPGA board allocation.
- Data signals can be used for differential pairs or single ended signals.



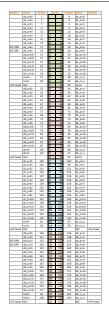
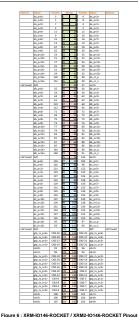


Figure 5: XRM-IO146 / XRM2-IO146 Pinout







5 Positive Locking / Cable Clamp Assembly

The XRN-IO146 uses a MICTOR connector with a minimum retention force of 4.75bs. For applications requiring positive locking on this connector or further strain relief, an optional locking assembly is available. This clearing interfaces with the housing on the cable assembly to lock it in place, and also provides strain relief to the cable itself by clamping it with protective material.

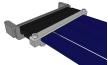


Figure 7 : MET-BEZEL-IO146-LOCK Top View

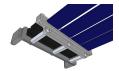


Figure 8 : MET-BEZEL-IO146-LOCK Bottom View



6 Product Codes

The following table describes the standard product codes and some of the available build options. Contact Alpha Data for further options, customizations, conformal coating, etc.

Product Code	Description
XRM-IO146	Standard module for Virtex-5 and earlier Alpha Data boards
XRM2-IO146	Standard module for Virtex-6 and later Alpha Data boards
XRM-IO146A	Standard module for ADM-XRC-4 board only
XRM-IO146-G	DCI enabled option for Virtex-4 and earlier
XRM(2)-IO146-ROCKET	High Speed Serial Lanes Option
XRM(2)-IO146-LOCK	Includes cable retention / positive locking assembly
XRM(2)-IO146-ROCKET-LOCK	HSS and includes cable retention / positive locking assembly
XRM(2)-IO146-[code]	Other build options as assigned by Alpha Data
Product Code	Accessory Description
XRM-IO146-LOOPBACK	Test adapter to loop back signals (not compatible with -ROCKET)
CAB-MICTOR-152	Cable assembly - 152M to 152M, 50cm
CAB-MICTOR-152M-4X38	Cable assembly - 152M split out to 4 x 38M, 50cm
MET-BEZEL-IO146-LOCK	Spare cable retention / positive locking assembly

Table 1 : Ordering Options



Revision History

Date	Revision	Nature of Change
2008	1.0	Initial Release
Nov 2012	2.0	Converted to XML, included XRM2 options, updated Mictor cable source, added cable retention option, added ROCKET option, created new pinout tables

Address: 4 West Silvermills Lane, Edinburgh, EH3 58D, UK Telephone: +44 131 558 2500 Fax: +44 131 558 2700 email: sales @alpha-data.com http://www.alpha-data.com Address: 3507 Ringsby Court Suite 105, Denver, CO 80216
Telephone: (303) 954 8788
Fax: (866) 820 9956 toll free salles (866) 820 9956 toll free website: http://www.abpha-data.com